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U.S. UTILITY Patent Application

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APPLICATION NO. CONT/PRIOR CLASS SUBCLASS ART UNIT EXAMINER 257 343 H · WEIS

James Beasom

Self-alignment of seperated regions in a lateral MOSFET structure of an integrated circuit $_$

BEST AVAILABLE CC.

PTO-2040 12/99

ISSUING CLASSIFICATION								
ORIGINAL	CROSS REFERENCE(S)							
CLASS	CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)							
257 343		4.						
INTERNATIONAL CLASSIFICATION								
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TERMINAL.	DRAWINGS			CLAIMS ALLOWED		
└─ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
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☐ The term of this patent	(date) Patent Expunines 1/1/3		NOTICE OF ALLOWANCE MALLED			
subsequent to (date)			7/103		1	
has been disclaimed.	(Assistant E	yaminer)	(Date)	22/18	13	
☐ The term of this patent shall		111			4	
not extend beyond the expiration date of U.S Patent. No	N////		ISSUE FEE			
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